

**IN THE CLAIMS:**

Please amend claims 1, 2, 4 and 6 as shown below

1. (currently amended) A liquid crystal display device comprising:  
a pair of substrates at least one of which is transparent and between which a liquid crystal layer is interposed; a plurality of gate lines and a plurality of drain lines being formed on an inner surface of one of the pair of substrates;  
a thin film transistor being disposed at an intersecting portion of one of the plurality of gate lines and one of the plurality of drain lines;  
a pixel electrode being connected to the thin film transistor;  
a counter electrode being disposed opposite to the pixel electrode;  
wherein each of the gate lines has a dimension in a direction of an extension direction of the drain lines which is substantially constant between adjacent drain lines;  
an edge portion of a semiconductor layer situated under a source electrode of the thin film transistor is arranged within an edge of the gate line situated under the semiconductor layer; and  
the semiconductor layer has a first portion and a second portion, a dimension of the first portion in the extension direction of the drain lines being narrower than a dimension of the second portion in the extension direction of the drain lines.
2. (currently amended) A liquid crystal display device according to claim 1, wherein a width of the second portion in an extension direction of the gate lines at a position where the semiconductor layer is situated under the source electrode is broader than that of the source electrode.

3. (original) A liquid crystal display device according to claim 1, wherein the semiconductor layer of the thin film transistor is separated from a semiconductor layer being disposed at the intersection portion of the gate line and the drain line.

4. (currently amended) A liquid crystal display device according to claim 3, wherein the thin film transistor has a drain electrode branching off from the one of the plurality of drain lines at a location outside the gate line situated under the semiconductor layer, covering a corner of the semiconductor layer at a side of the one of the plurality of drain lines, and extending over the semiconductor layer, and the semiconductor layer has two directional getting over ~~portion~~ portions where the drain electrode gets over an edge of the semiconductor layer in two different directions with respect to an extension direction of the drain electrode.

5. (original) A liquid crystal display device according to claim 3, wherein the semiconductor layer of the thin film transistor has three directional getting over portions where the drain electrode and the source electrode gets over an edge of the semiconductor layer in three different directions with respect to respective extension directions thereof.

6. (currently amended) A liquid crystal display device comprising:  
a pair of substrates at least one of which is transparent and between which a liquid crystal layer is interposed;

a plurality of pixel regions being disposed in a matrix manner along a first direction and a second direction transverse to the first direction on an inner surface of one of the pair of substrates, each of which has a gate electrode, an insulating layer being formed over the gate electrode, a semiconductor layer being formed on the insulating layer, source and drain electrodes being formed on the semiconductor

layer and spaced from one another, and a pixel electrode being connected to one of the source and drain electrodes;

a plurality of gate signal lines a respective one of which is connected to the gate electrode of each of the pixel regions being arranged along the first direction; and

a plurality of video signal lines a respective one of which is connected to another of the source and drain electrodes of each of the pixel regions being arranged in the second direction,

wherein each of the gate lines has a dimension in a direction of an extension direction of the video signal lines which is substantially constant between adjacent video signal lines;

the semiconductor layer is formed within a contour of the gate electrode in each of the pixel regions; and

the semiconductor layer has a first portion and a second portion, a dimension of the first portion in the extension direction of the video signal lines being narrower than a dimension of the second portion in the extension direction of the video signal lines.

7. (original) A liquid crystal display device according to claim 6, wherein each of the plurality of video signal lines are formed on another semiconductor layer being formed so as to be spaced from the semiconductor layer of each of the pixel regions.

8. (original) A liquid crystal display device according to claim 6, further comprising a counter electrode being formed on an inner surface of another of the pair of substrates, wherein each of the pixel electrodes is opposite to the counter electrode across the liquid crystal layer.